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U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

JC784 U.S. PTO
03/20/00

JC511 U.S. PTO
09/531910
03/20/00

UTILITY PATENT APPLICATION
TRANSMITTAL LETTER
UNDER 37 C.F.R. 1.53(b)

ATTORNEY DOCKET NO.:
2207/7896

Address to:

Box PATENT APPLICATION
Assistant Commissioner for Patents
Washington D.C. 20231

Transmitted herewith for filing is the patent application of

Inventor(s): **Sitaram Yadavalli and Sandip Kundu**

For : **METHOD AND APPARATUS FOR MODELING AND CIRCUITS WITH
ASYNCHRONOUS BEHAVIOR**

1. Enclosed are:

- a) 1 cover sheet, 9 sheets of specification, 3 sheets of claims, and 1 sheet of abstract;
- b) 7 sheets of drawings;
- c) Declaration and Power of Attorney (signed);
- d) Assignment
- e) Express Mail Certificate; and
- f) Return Receipt Postcard.

2. The filing fee has been calculated as shown below:

	NUMBER FILED	NUMBER EXTRA*	RATE (\$)	FEE (\$)
BASIC FEE				690.00
TOTAL CLAIMS	21 - 20 =	1	18.00	18.00
INDEPENDENT CLAIMS	3 - 3 =	0	78.00	.00
MULTIPLE DEPENDENT CLAIM PRESENT			260.00	0
*Number extra must be zero or larger		TOTAL		708.00
If the applicant is a small entity under 37 C.F.R. §§ 1.9 SMALL ENTITY and 1.27, then divide total fee by 2, and enter amount here.		TOTAL		

3. Please charge the required application filing fee of \$ **708.00** to the deposit account of **Kenyon & Kenyon**, deposit account number **11-0600**.
4. The Commissioner is hereby authorized to charge payment of the following fees, associated with this communication or arising during the pendency of this application, or to credit any overpayment to the deposit account of **Kenyon & Kenyon**, deposit account number **11-0600**:
- Any additional filing fees required under 37 C.F.R. § 1.16;
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 - Any additional document supply fees under 37 C.F.R. § 1.19;
 - Any additional post-patent processing fees under 37 C.F.R. § 1.20; or
 - Any additional miscellaneous fees under 37 C.F.R. § 1.21.
5. A duplicate copy of this sheet is enclosed.

Dated: 3-20-00

By: 
Jerray Wei (Reg. No. 43,247)

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TYPE OF DOCUMENT **METHOD AND APPARATUS FOR MODELING AND CIRCUITS WITH ASYNCHRONOUS BEHAVIOR**

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